

10/039637  
12/31/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10039637	FILING DATE 12/31/2001	CLASS 200	SUBCLASS 10	GAU 2424	EXAMINER Bowers
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\*\*CONTINUING DATA VERIFIED:

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\*\* FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		42390.P12454
Verified and Acknowledged Examiners's initials		
TITLE : Method and apparatus for layout synthesis of regular structures using relative placement		
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
PREPARED FOR ISSUE		Sheets Drawg.	Figs. Drawg.
Application Examiner		Print Fig.	
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